

**S/N 10/789,800**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kevin Torek et al. Examiner: Tu-Tu V. Ho  
Serial No.: 10/789,800 Group Art Unit: 2818  
Filed: February 27, 2004 Docket: 303.871US1  
Title: SEMICONDUCTOR FABRICATION THAT INCLUDES SURFACE TENSION  
CONTROL

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**COMMUNICATION CONCERNING RELATED APPLICATION(S)**

**MS AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11217037	August 31, 2005	303.871US2	SEMICONDUCTOR FABRICATION THAT INCLUDES SURFACE TENSION CONTROL
11216740	August 31, 2005	303.871US3	SEMICONDUCTOR FABRICATION THAT INCLUDES SURFACE TENSION CONTROL

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,  
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19 June '06

By

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19 day of June, 2006.

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